FIG.1 Related Art

lead−in area	DMA1	
	reserved DMA2	
	reserved	
data area	user area of zone0	spare area
	user area of zone1	spare area
		•
	user area of $zone(N-1)$	spare area
	user area of zoneN	spare area
lead-out area	DMA3	
	reserved	
	DMA4	
	reserved	

FIG.2A Related Art

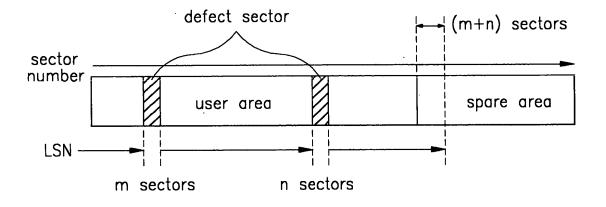


FIG.2B Related Art

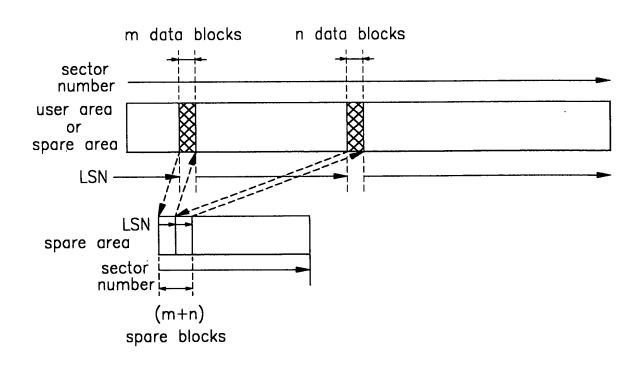


FIG.3 Related Art

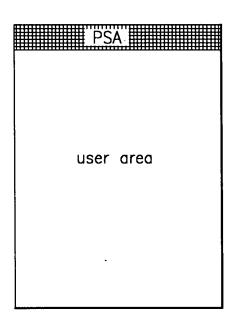


FIG.4 Related Art

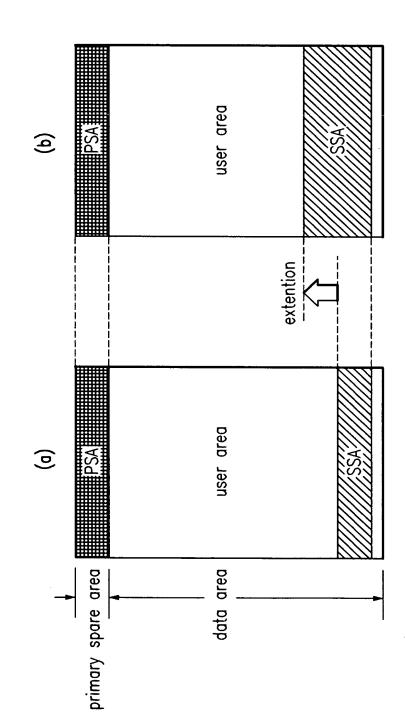


FIG.5A Related Art

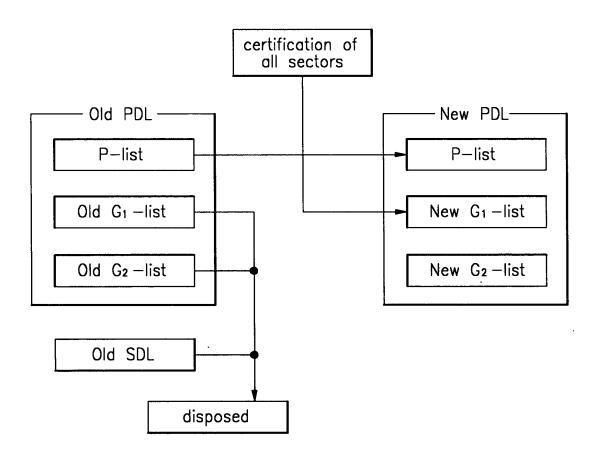
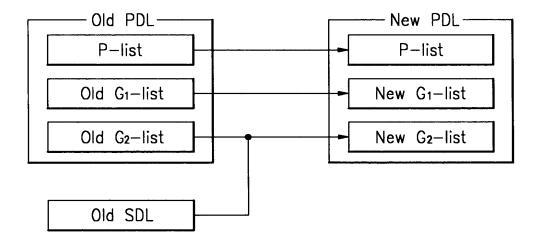


FIG.5B Related Art



í,

FIG.6

